



# Program termination · Lecture 2

Berkeley · Spring '09

Byron Cook



## Review from the previous lecture

- Program termination = WF transition relation
- Proving WF can be reduced to finding a larger *ranking relation*
- Accurate transition relations often too hard to compute
  - Supporting invariants needed to establish termination
- Unions of WF-relations not WF, but transitive closure can be used to offset the problem

## Review from the previous lecture

- We can use variables with finite range to decompose termination proofs (*e.g.* the program counter)
- Linear ranking function synthesis is decidable
  - But linear ranking functions are often not enough.....

## Review from the previous lecture

- Finding linear ranking functions (for relations with only linear updates and conditions) is decidable
- Not all WF linear relations have linear ranking functions, *e.g.*
  - $R \triangleq x > 0 \wedge x' = x - y \wedge y' = y + 1$
  - $R \triangleq x \geq 0 \wedge x' = -2x + 10$
  - Ackermann's function
  - .....



# Overview

- Notes on a representation for programs
- Checking termination arguments
- Refining termination arguments
- Induction
- Termination analysis

# Programs

$$\llbracket x := e \rrbracket_V \triangleq \{(s, t) \mid \forall v \in V - \{x\}. s(v) = t(v) \wedge t(x) = e\}$$

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$$\llbracket x := * \rrbracket_V \triangleq \{(s, t) \mid \forall v \in V - \{x\}. s(v) = t(v)\}$$

$$\llbracket \text{assume}(e) \rrbracket_V \triangleq \{(s, t) \mid \forall v \in V. s(v) = t(v) \wedge e[s]\}$$

# Programs

- Programs are rooted cyclic graphs where edges are annotated with finite command sequences
- A “cutpoint set” can be computed by uniquely numbering the nodes and marking each node that can transition to a node that’s lower in the order

# Programs

- The meaning of the program is a relation constructed from the graph and commands
- A special variable (not used in the program) **pc** is used to track program location
- Paths are sequences of **pc** valuations, traces are sequences of commands drawn from paths

# Programs

The *relational meaning* of a finite path/trace segment is the relational composition of the meaning of each command.

$$\llbracket c_1; c_2; \dots \rrbracket_{V, \rho} \triangleq \llbracket c_1 \rrbracket_{V, \rho} ; \llbracket c_2 \rrbracket_{V, \rho} ; \dots$$

# Programs

The *relational*  
ment is the  
of each comm

$\llbracket c_1; c_2; \dots \rrbracket_{V, R}$

$(s, t)$  s.t.  $\exists x_0, x_1, y_0.$

$$\wedge \left\{ \begin{array}{l} s(x) = x_0 \\ s(y) = y_0 \\ x_1 = x_0 + y_0 \\ x_1 > 0 \\ t(x) = x_1 \\ t(y) = y_0 \end{array} \right\}.$$

$\llbracket$   $x := x + y;$   
 $\mathbf{assume}(x > 0);$   $\rrbracket$



# Programs

The *relational*  
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# Overview

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# Transformations to reachability supporting closure

Defining  $\boxplus$  such that

$$(R_{*I}^{\dagger})_{\downarrow \text{pc}=k} \subseteq Q \text{ iff } \neg \text{REACHABLE}_{\boxplus}(\mathcal{P}, k, Q)(\ell_{err})$$

# Transformations to reachability supporting closure

Defining  $\boxplus$  such that

$$(R|_I^+) \downarrow_{\text{pc}=k} \subseteq Q \text{ iff } \neg \text{REACHABLE}_{\boxplus}(\mathcal{P}, k, Q)(\ell_{err})$$

Tools like SLAM,  
BLAST, IMPACT,  
F-Soft, etc.



# Transformations to reachability supporting closure

New program

Defining  $\boxplus$  such that

$$(R_{*I}^{\dagger})_{\downarrow \text{pc}=k} \subseteq Q \text{ iff } \neg \text{REACHABLE}_{\boxplus}(\mathcal{P}, k, Q, \ell_{\text{err}})$$

# Transformations to reachability supporting closure


Supports closure

Defining  $\boxplus$  such that

$$(R_{*I}^{\boxplus}) \downarrow_{pc=k} \subseteq Q \text{ iff } \neg \text{REACHABLE}_{\boxplus}(\mathcal{P}, k, Q)(l_{err})$$

# Transformations to reachability supporting closure

Defining  $\boxplus$  such that

$$\underline{(R|_I^+)}_{\downarrow \text{pc}=k} \subseteq Q \text{ iff } \neg \text{REACHABLE}_{\boxplus}(\mathcal{P}, k, Q)(\ell_{err})$$


Using decomposition

strategy we prove termination

## Reachability engines

**Definition.** Assume  $\llbracket \mathcal{P} \rrbracket = (I, R, S)$ . A reachability engine  $\text{REACHABLE}_{\mathcal{P}}(k)$  returns **false** when

$$R^*(I) \cap \llbracket \text{pc} = k \rrbracket = \emptyset$$

If  $\text{REACHABLE}_{\mathcal{P}}(k) = \mathbf{true}$ , then a witness path from 0 to  $k$  is returned.



# Transformations to reachability supporting closure

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# Transformations to reachability supporting closure

Defining  $\boxplus$  such that

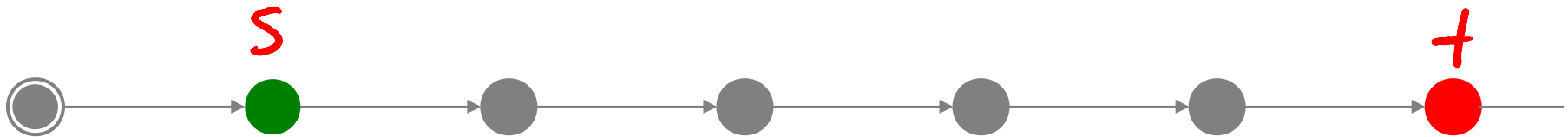
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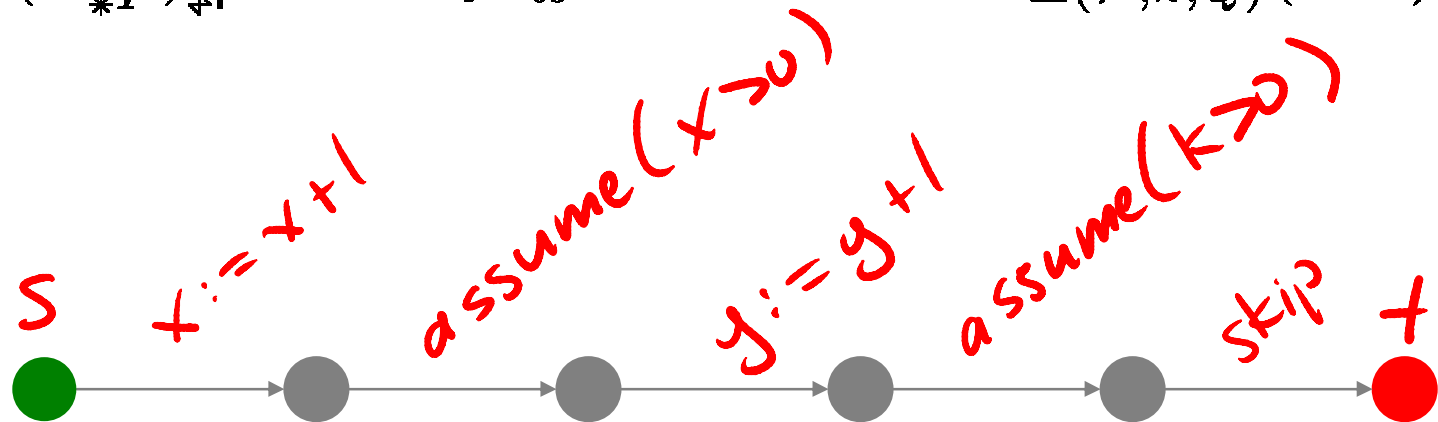
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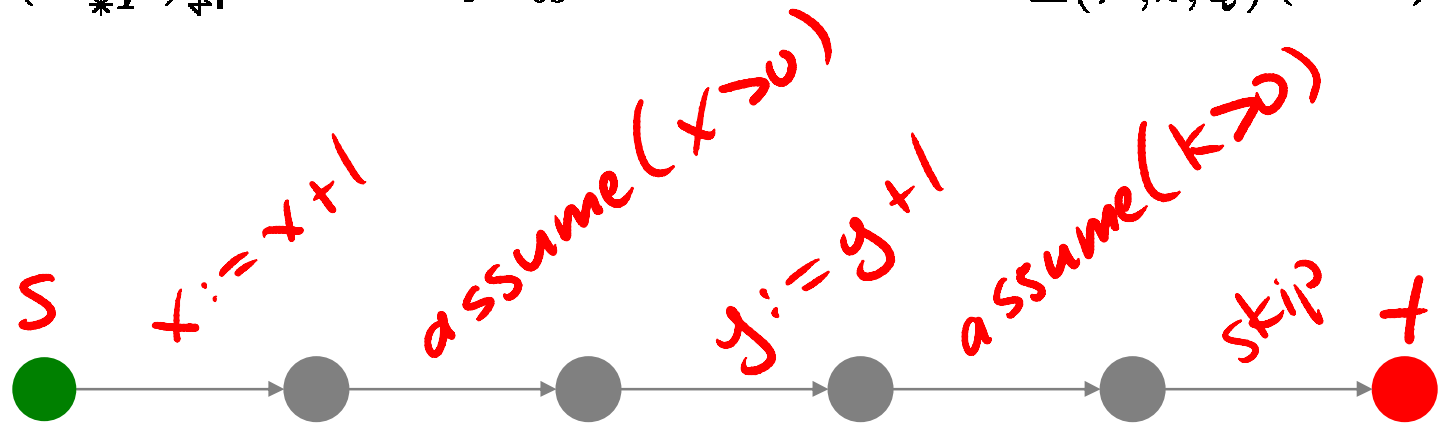
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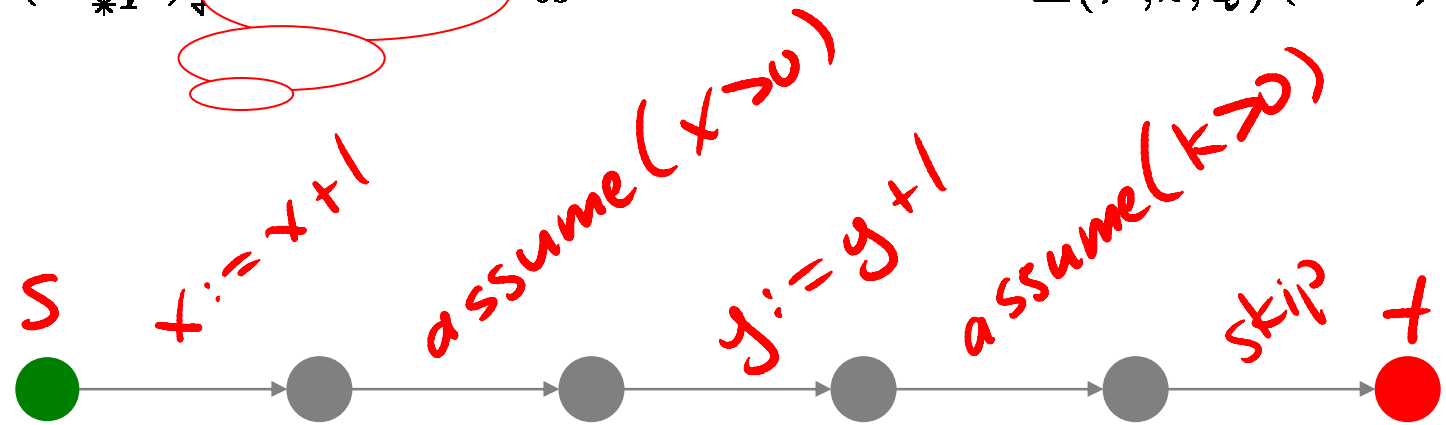
is  $(s, t) \in \mathcal{Q}$ ?

# Transformations to reachability supporting closure

$\llbracket x := x+1; \text{assume}(x > 0); \dots \rrbracket^* R(\pm)$   
 $\leq \mathcal{Q}?$

Def.

$(R \uparrow^+)_* I \downarrow \mathcal{Q} \text{ iff } \text{REACHABLE}_{\boxplus}(\mathcal{P}, k, \mathcal{Q})(l_{err})$



is  $(s, t) \in \mathcal{Q}?$

# Transformations to reachability supporting closure

```
copied = 0;  
.  
.  
.  
  
while(*) {  
  k: loop body  
  if (copied==0) {  
  }  
    if (*) {  
      old_x = x;  
      old_y = y;  
      .  
      .  
      copied = 1;  
    }  
  } else {  
    assert(Q);  
  }  
}
```



# Transformations to reachability supporting closure

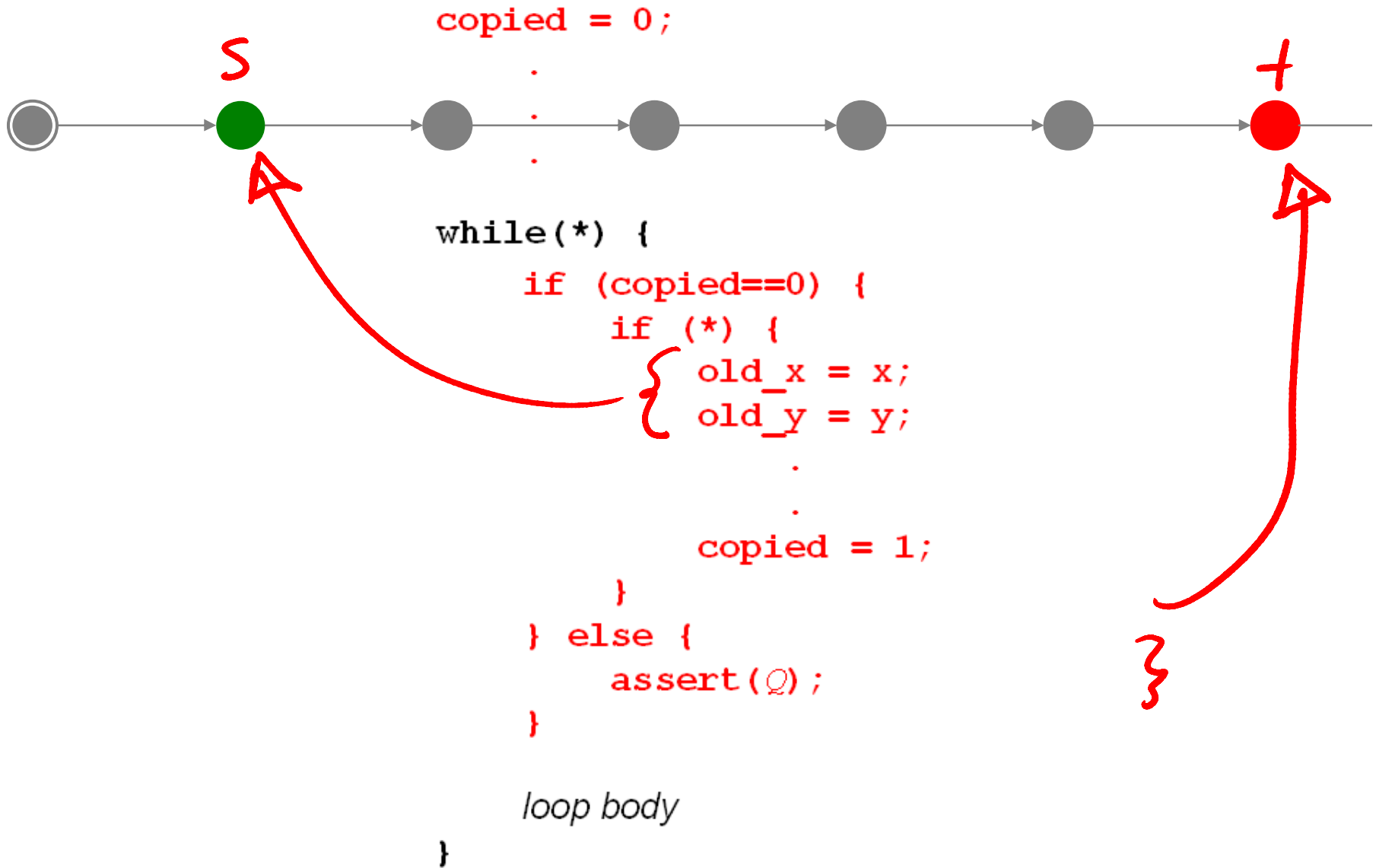
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## Transformations to reachability supporting closure

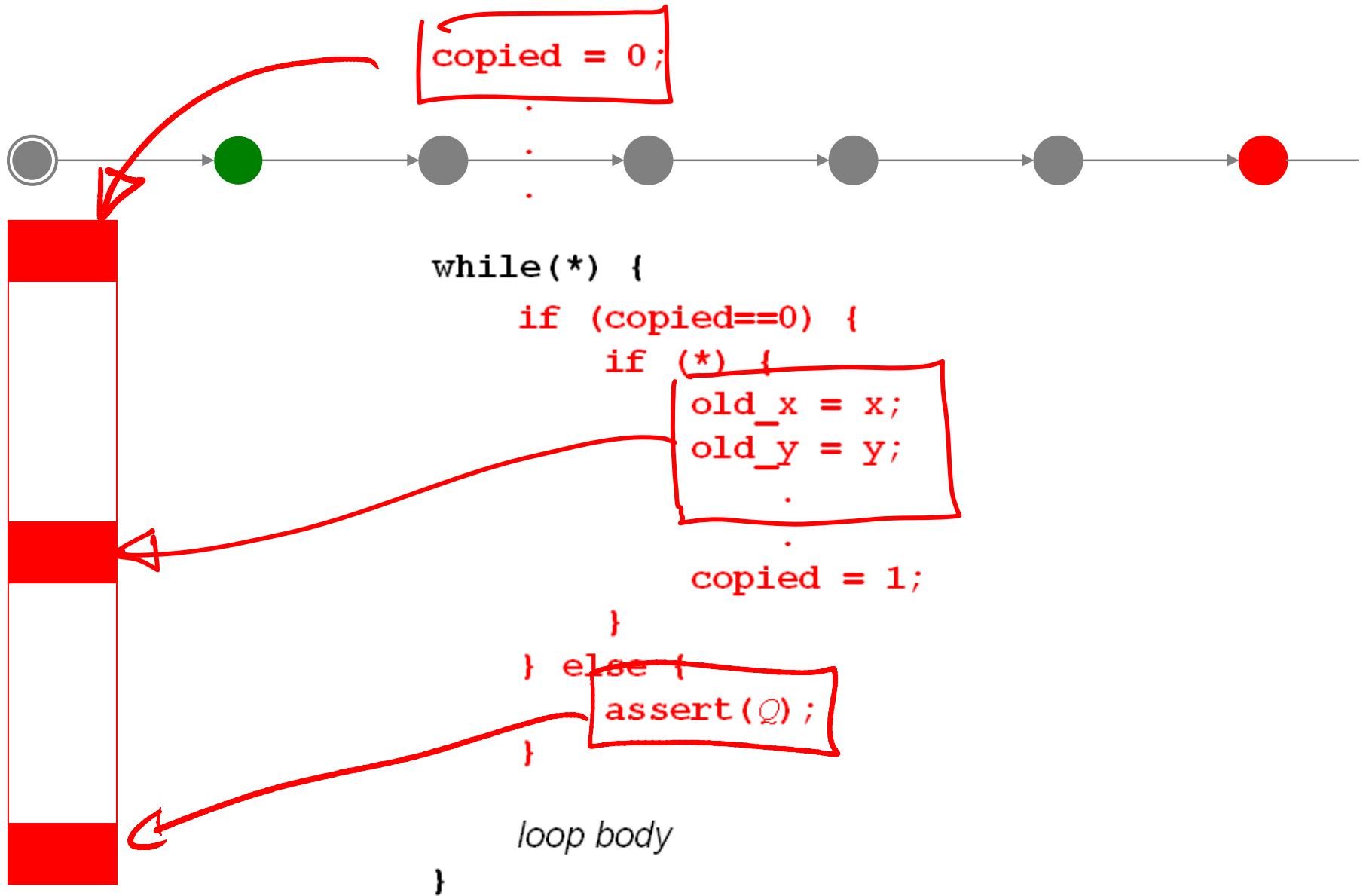
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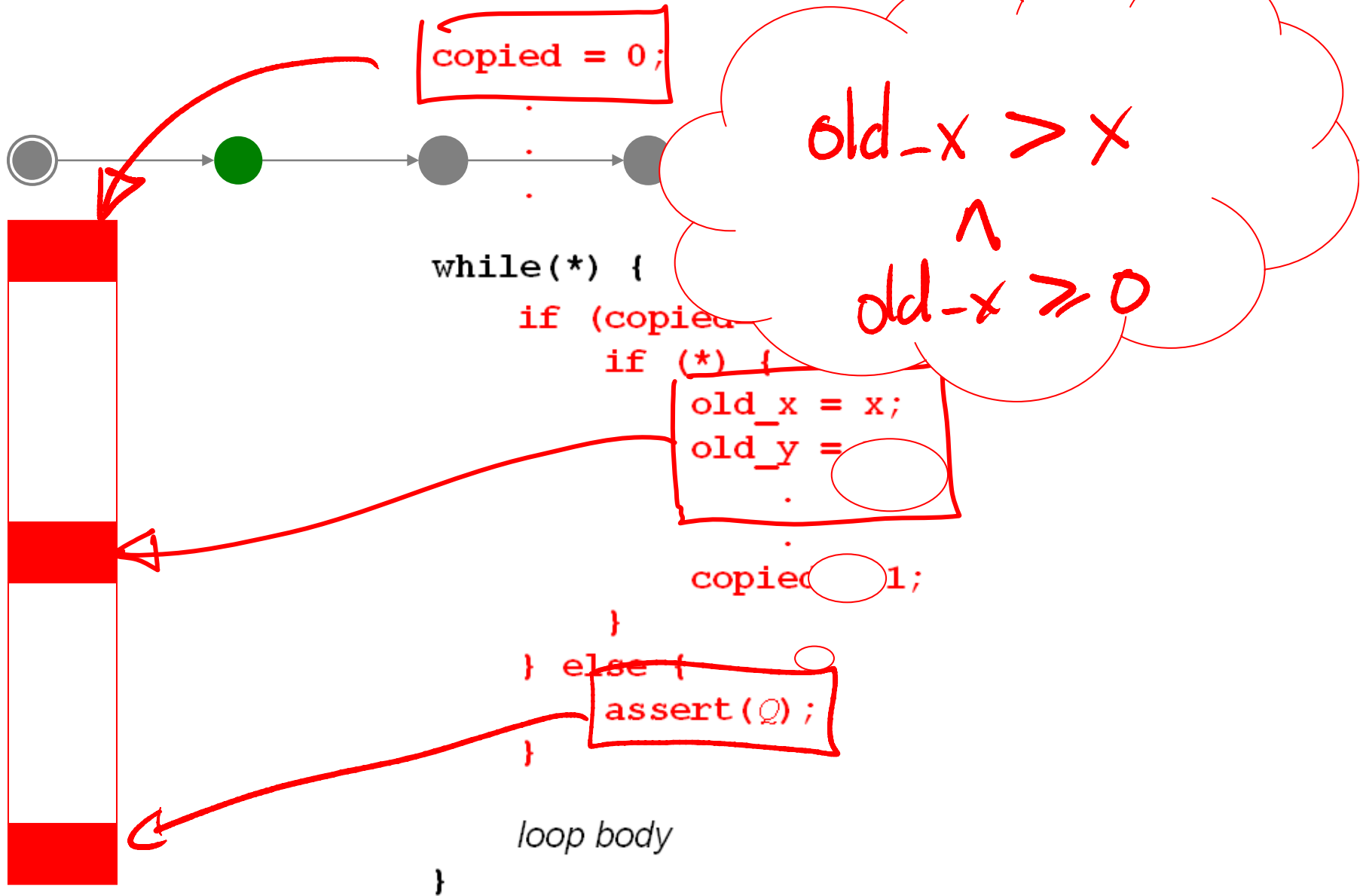
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# Transformations to reachability supporting closure



# Transformations to reachability supporting closure



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http://research.microsoft.com/en-us/um/cambridge/projects/terminator/binreach.pdf

# Termination Proofs for Systems Code \*

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## Abstract

Program termination is central to the process of ensuring that systems code can always react. We describe a new program termination prover that performs a path-sensitive and context-sensitive program analysis and provides capacity for large program fragments (i.e. more than 20,000 lines of code) together with support for programming language features such as arbitrarily nested loops, pointers, function-pointers, side-effects, etc. We also present experimental results on device driver dispatch routines from the Windows operating system. The most distinguishing aspect of our tool is how it shifts the balance between the two tasks of *constructing* and respectively *checking* the termination argument. Checking becomes the hard step. In this paper we show how we solve the corresponding challenge of *checking* with *binary reachability analysis*.

**Categories and Subject Descriptors** D.2.4 [Software]: Software Engineering—Program Verification; D.4.5 [Software]: Operating Systems—Reliability

request packet and `FdoData->TopOfStack` is the pointer to another serial-based device driver). In the case where the other device driver returns a return-value that indicates success, but places 0 in `PIoStatusBlock->Information`, the serial enumeration driver will fail to increment the value pointed to by `nActual` (line 66), possibly causing the driver to infinitely execute this loop and not return to its calling context. The consequence of this error is that the computer's serial devices could become non-responsive. Worse yet, depending on what actions the other device driver takes, this loop may cause repeated acquiring and releasing of kernel resources (memory, locks, etc) at high priority and excessive physical bus activity. This extra work stresses the operating system, the other drivers, and the user applications running on the system, which may cause them to crash or become non-responsive too.

This example demonstrates how a notion of termination is central to the process of ensuring that reactive systems can always react. Until now no automatic termination tool has ever been able to provide a capacity for large program fragments (>20,000 lines)

Done

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# Isolation

Nesting of loops allows us to isolate pieces of the program

- When proving well-foundedness of cutpoints in inner loops, we can ignore non-termination of the enclosing loop
- When proving well-foundedness of cutpoints in outer loops, we can ignore non-termination of the inner loop

```
while(x<y) {  
    k = nondet();  
    while(k>0) {  
        k--;  
    }  
    x++;  
}
```

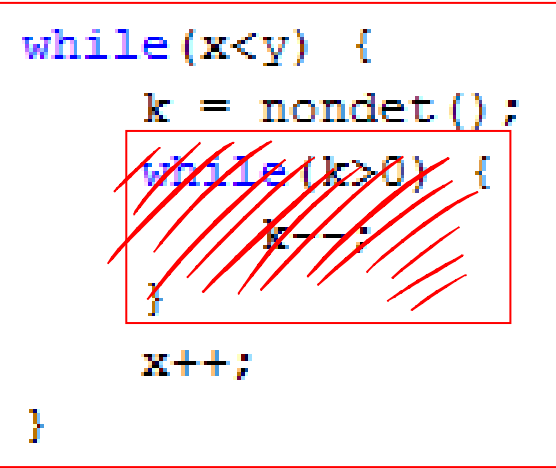


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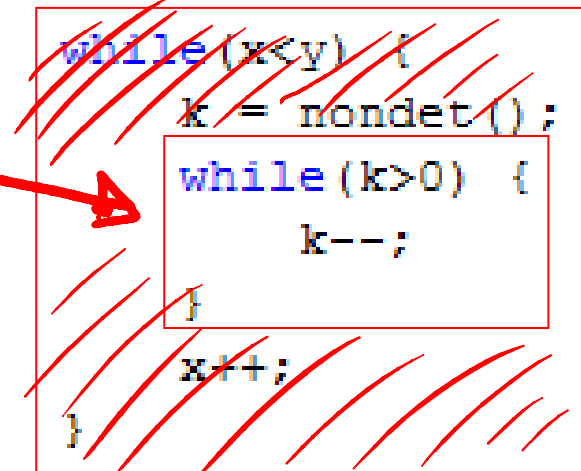
*Comes for free*

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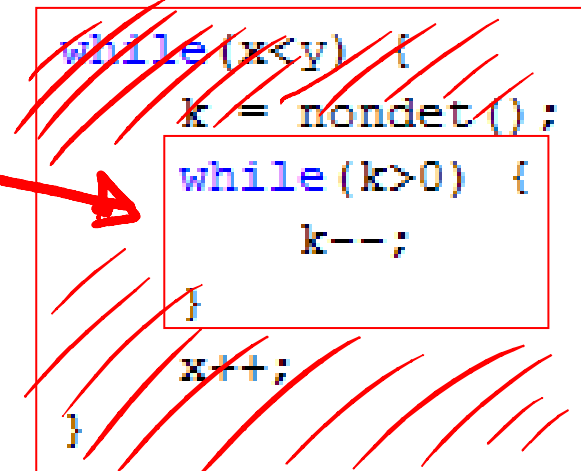


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        k--;  
    }  
    x++;  
}
```

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→ When proving well-foundedness of cutpoints in inner loops, we can ignore non-termination of the enclosing loop



```
while(x<y) {  
    k = nondet();  
    while(k>0) {  
        k--;  
    }  
    x++;  
}
```

→ When proving well-foundedness of cutpoints in outer loops, we can ignore non-termination of the inner loop

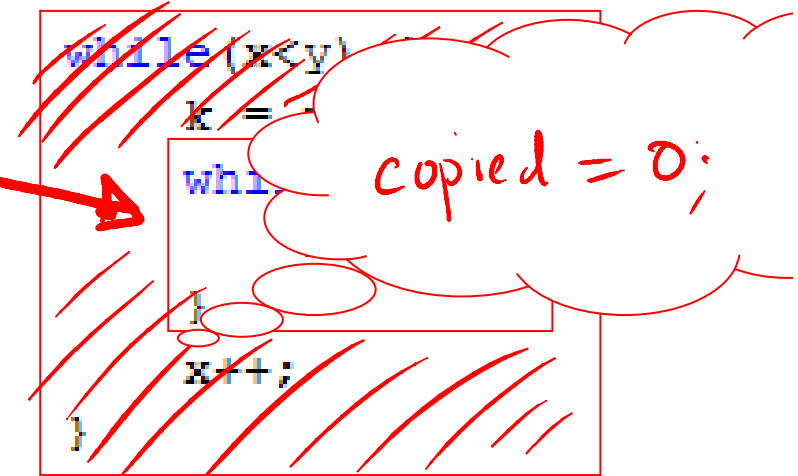
We don't support this yet.....

# Isolation

Nesting of loops allows us to isolate pieces of the program

→ When proving well-foundedness of cutpoints in inner loops, we can ignore non-termination of the enclosing loop

→ When proving well-foundedness of cutpoints in outer loops, we can ignore non-termination of the inner loop



We don't support this yet.....

# Transformations to reachability supporting closure

```
while(*) {  
    if (copied==0) {  
        if (*) {  
            old_x = x;  
            old_y = y;  
            .  
            .  
            copied = 1;  
        }  
    } else {  
        assert(Q);  
    }  
  
    loop body  
}  
copied = 0;
```

# Transformations to reachability supporting closure

```
while(*) {  
    if (copied==0)  
        *  
    }  
}
```

Decomposition  
story now more  
complex.....

`copied = 0;`

## Transformations to reachability supporting closure

" $l$  is not visited infinitely often so long as the context is not entered infinitely often"

```
loop b  
}  
copied = 0;
```



# Overview

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## Rank function synthesis

**Definition.**  $\text{SYNTHESIS} : (S \leftrightarrow S) \rightarrow (S \rightarrow \mathbb{N})$  is a partial function such that  $R \subseteq \sqsupseteq_{\text{SYNTHESIS}(R)}$  when  $\text{SYNTHESIS}(R)$  is defined.

# Refinement

```
 $T := \emptyset$   
while REACHABLE $_{\boxplus(\mathcal{P}, \ell, T)}(\ell_{err})$  do  
  let  $\pi_s, \pi_c =$  lasso in  $\boxplus(\mathcal{P}, \ell, T)$  from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^*(\llbracket \pi_s \rrbracket))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \downarrow_{\rho}$ ) returns ranking relation  $f$  then  
     $T := T \cup \succeq_f$   
  else  
    report “potential counterexample found:  $\pi_s, \pi_c$ ”  
  fi  
od  
report “termination proved with argument  $T$ ”
```

## Refinement

$$\rho \supseteq \llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket)$$

```
 $T := \emptyset$   
while REACHABLE( $\rho$ )  
  let  $\pi_s, \pi_c$  in  $\mathbb{B}(\mathcal{P}, \mathcal{C}, \mathcal{I})$ , from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \downarrow \rho$ ) returns ranking relation  $f$  then  
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# Refinement

$$\rho \supseteq \llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket)$$

$T := \emptyset$

**while** REACHABLE

**let**  $\pi_s, \pi_c$  **in** E

**let**  $\rho = \alpha(\llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket))$

**if** SYNTHESIS( $\llbracket \pi_c \rrbracket \downarrow \rho$ )

$T := T \cup \{ \rho \}$

**else**

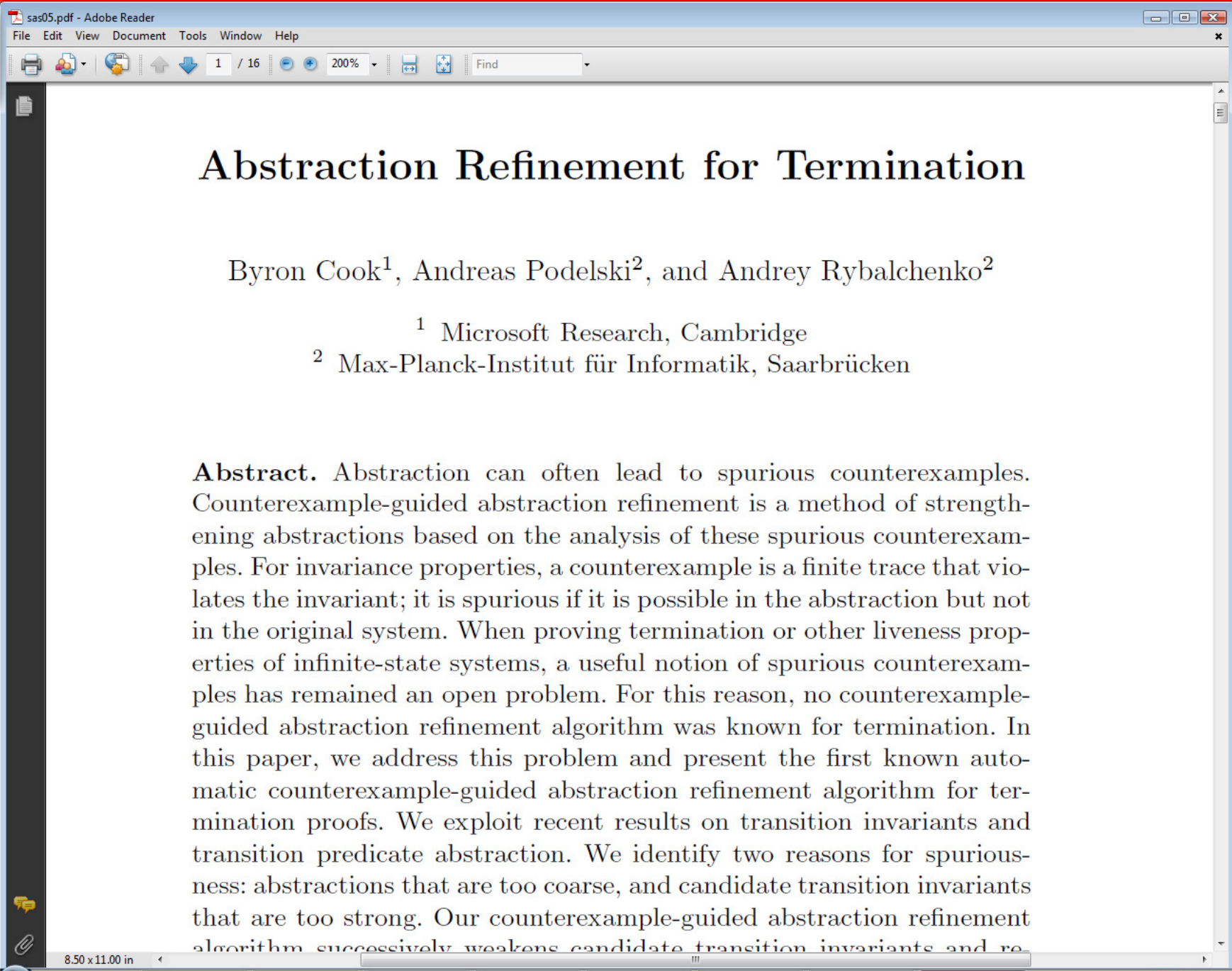
**report** “potential counterexample found:  $\pi_s, \pi_c$ ”

**fi**

**od**

**report** “termination proved with argument  $T$ ”

$$\llbracket \pi_c \rrbracket \downarrow \llbracket \pi_s \rrbracket \subseteq \llbracket \pi_c \rrbracket \downarrow \rho$$



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# Abstraction Refinement for Termination

Byron Cook<sup>1</sup>, Andreas Podelski<sup>2</sup>, and Andrey Rybalchenko<sup>2</sup>

<sup>1</sup> Microsoft Research, Cambridge  
<sup>2</sup> Max-Planck-Institut für Informatik, Saarbrücken

**Abstract.** Abstraction can often lead to spurious counterexamples. Counterexample-guided abstraction refinement is a method of strengthening abstractions based on the analysis of these spurious counterexamples. For invariance properties, a counterexample is a finite trace that violates the invariant; it is spurious if it is possible in the abstraction but not in the original system. When proving termination or other liveness properties of infinite-state systems, a useful notion of spurious counterexamples has remained an open problem. For this reason, no counterexample-guided abstraction refinement algorithm was known for termination. In this paper, we address this problem and present the first known automatic counterexample-guided abstraction refinement algorithm for termination proofs. We exploit recent results on transition invariants and transition predicate abstraction. We identify two reasons for spuriousness: abstractions that are too coarse, and candidate transition invariants that are too strong. Our counterexample-guided abstraction refinement algorithm successively weakens candidate transition invariants and re-

8.50 x 11.00 in

## Trace Tree

```
... 9: nondet
... 10: nondet
... 12: nondet
... 13: nondet
... 15: while(x>0 && y>0) {
... 15: while(x>0 && y>0) {
... 17: nondet
... 17: if (nondet()) {
... 21: y--;
... 24: if (copied==0) {
... 25: nondet
... 25: if (nondet()) {
... 26: oldx = x;
... 27: oldy = y;
... 28: copied=1;
... 15: while(x>0 && y>0) {
... 15: while(x>0 && y>0) {
... 17: nondet
... 17: if (nondet()) {
... 21: y--;
... 24: if (copied==0) {
... 31: if (!0) {
```

Step: 0

State

## Source Code

example1.c

```
... 12: x = nondet();
... 13: y = nondet();
... 14:
... 15: while(x>0 && y>0) {
... 16:
... 17:     if (nondet()) {
... 18:         y++;
... 19:         x--;
... 20:     } else {
... 21:         y--;
... 22:     }
... 23:
... 24:     if (copied==0) {
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... 26:             oldx = x;
... 27:             oldy = y;
... 28:             copied=1;
... 29:         }
... 30:     } else {
... 31:         if (!0) {
... 32:             SLIC_ERROR:0;
... 33:         }
... 34:     }
... 35: }
```

```
while(x>0 && y>0) {  
  
    if (nondet()) {  
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        y--;  
    }  
}
```



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25: if (nondet()) {
26: oldx = x;
27: oldy = y;
28: copied=1;
15: while(x>0 && y>0) {
15: while(x>0 && y>0) {
17: nondet
17: if (nondet()) {
21: y--;
24: if (copied==0) {
31: if (!0) {
    
```

*lls*

*llc*

Source Code

example1.c

```

12: x = nondet();
13: y = nondet();
14:
15: while(x>0 && y>0) {
16:
17:     if (nondet()) {
18:         y++;
19:         x--;
20:     } else {
21:         y--;
22:     }
23:
24:     if (copied==0) {
25:         if (nondet()) {
26:             oldx = x;
27:             oldy = y;
28:             copied=1;
29:         }
30:     } else {
31:         if (!0) {
32:             SLIC_ERROR:0;
33:         }
34:     }
35: }
    
```

Step: 0

State

Static Driver Verifier Defect Viewer.

File View Trace Tree Help

Trace Tree

```

9: nondet
10: nondet
12: nondet
13: nondet
15: while(x>0 && y>0)
15: while(x>0 && y>0)
17: nondet
17: if (nondet()) {
21: y--;
24: if (copied==0) {
25: nondet
25: if (nondet()) {
26: oldx = x;
27: oldy = y;
28: copied=1;
15: while(x>0 && y>0) {
15: while(x>0 && y>0) {
17: nondet
17: if (nondet()) {
21: y--;
24: if (copied==0) {
31: if (!0) {

```

Step: 0

State

$$[\pi_c](oldx, oldy), (x, y) = \exists x_0, y_0, y_1.$$

$$\wedge \left\{ \begin{array}{l} oldx = x_0 \\ oldy = y_0 \\ x_0 > 0 \\ y_0 > 0 \\ y_1 = y_0 - 1 \\ x = x_0 \\ y = y_1 \end{array} \right.$$

```

23:
24:     if (copied==0) {
25:         if (nondet()) {
26:             oldx = x;
27:             oldy = y;
28:             copied=1;
29:         }
30:     } else {
31:         if (!0) {
32:             SLIC_ERROR:0;
33:         }
34:     }
35: }

```

File: ?, Line: 0, Function "

*llc*



Produces ranking function y, 0

$$\wedge \left\{ \begin{array}{l} \text{oldx} = x_0 \\ \text{oldy} = y_0 \\ x_0 > 0 \\ y_0 > 0 \\ y_1 = y_0 - 1 \\ x = x_0 \\ y = y_1 \end{array} \right\}$$

```
17: ...
21: y--;
24: if (copied==0) {
25: nondet
25: if (nondet()) {
26: oldx = x;
27: oldy = y;
28: copied=1;
```

```
15: while(x>0 && y>0) {
15: while(x>0 && y>0) {
17: nondet
17: if (nondet()) {
21: y--;
24: if (copied==0) {
31: if (!0) {
```

llc

```
24: if (copied==0) {
25: if (nondet()) {
26: oldx = x;
27: oldy = y;
28: copied=1;
29: }
30: } else {
31: if (!0) {
32: SLIC_ERROR:0;
33: }
34: }
35: }
```

Step: 0

State

Static Driver Verifier Defect Viewer.

File View Trace Tree Help

Trace Tree

```

15: while(x>0 && y>0) {
15: while(x>0 && y>0) {
17: nondet
17: if (nondet()) {
18: y++;
19: x--;
24: if (copied==0) {
25: nondet
25: if (nondet()) {
26: oldx = x;
27: oldy = y;
28: copied=1;
15: while(x>0 && y>0) {
15: while(x>0 && y>0) {
17: nondet
17: if (nondet()) {
18: y++;
19: x--;
24: if (copied==0) {
31: if (!(0 || (oldy>=y+1 &&
31: if (!(0 || (oldy>=y+1 &&

```

Step: 0

State

Step:

State

Driver: Rule: Defect: Feasible Trace to SLIC\_ERROR

Source Code

example1.c

```

13: y = nondet();
14:
15: while(x>0 && y>0) {
16:
17:     if (nondet()) {
18:         y++;
19:         x--;
20:     } else {
21:         y--;
22:     }
23:
24:     if (copied==0) {
25:         if (nondet()) {
26:             oldx = x;
27:             oldy = y;
28:             copied=1;
29:         }
30:     } else {
31:         if (!(0 || (oldy>=y+1 && y>=0))) {
32:             SLIC_ERROR:0;
33:         }
34:     }
35: }
36:
37: }

```

File: ?, Line: 0, Function "

File: ?, Line: 0, Function "

$$(\text{oldx}, \text{oldy}), (x, y) = \exists x_0, y_0, y_1.$$

Static Driver Verifier Defect Viewer.

File View Trace Tree Help

Trace Tree

```
15: while(x>0 && y>0)
15: while(x>0 && y>0)
17: nondet
17: if (nondet()) {
18: y++;
19: x--;
24: if (copied==0) {
25: nondet
25: if (nondet()) {
26: oldx = x;
27: oldy = y;
28: copied=1;
15: while(x>0 && y>0) {
15: while(x>0 && y>0) {
17: nondet
17: if (nondet()) {
18: y++;
19: x--;
24: if (copied==0) {
31: if (!(0 || (oldy>=y+1 &&
31: if (!(0 || (oldy>=y+1 &&
```

Source Code

example1.c

```
13: y = nondet();
14:
15: while(x>0 && y>0) {
16:
17:     if (nondet()) {
18:         y++;
19:         x--;
20:     } else {
21:         y--;
22:     }
23:
24:     if (copied==0) {
25:         if (nondet()) {
26:             oldx = x;
27:             oldy = y;
28:             copied=1;
29:         }
30:     } else {
31:         if (!(0 || (oldy>=y+1 && y>=0))) {
32:             SLIC_ERROR:0;
33:         }
34:     }
35: }
36:
37: }
```

Step: 0

State

Step:

State

Driver: Rule: Defect: Feasible Trace to SLIC\_ERROR

New  
lasso

```

Cygwin
, acu 1, afu 1: G(t__y_1_1_2,0,0,1)=S(L(y,1,1,2),(0,29,2,0)), ((S(L(y,1,1,2),(0
,29,2,0))-1)-1)>0, L(y,1,1,2)=((S(L(y,1,1,2),(0,29,2,0))-1)-1), L(y,1,1,2)=S(L
(y,1,1,2),(0,29,2,0)), L(y,1,1,2)=((S(L(y,1,1,2),(0,29,2,0))-1)
SLAM: iter-begin 9 ~~~~~
AR: no new preds at iter 9
AR: calling constrain at iter 9
AR: calling bebop bebop -o -t -c -m -u -l SLIC_ERROR -f slam.bp >bebop.out 2>&1

AR: bebop generated a path
AR: calling bebop bebop -o -t -c -m -u -l SLIC_ERROR -f slam.bp >bebop.out 2>&1

AR: bebop generated a path
AR: calling bebop bebop -o -t -c -m -u -l SLIC_ERROR -f slam.bp >bebop.out 2>&1

AR: bebop generated a path
AR: calling bebop bebop -o -t -c -m -u -l SLIC_ERROR -f slam.bp >bebop.out 2>&1

AR: bebop is raising completed
AR: watch_startup_end Completed
SLAM: watch-startup-end 0, iter 9 ~~~~~
AR: saving preds
Program [ example.c ] passed property
Saving termination lemmas to "witness.tt"
Time: 218.408
$
    
```



# Terminator Lemma Viewer

File View Help

Proof Information

[-] Lemmas

[-] main

8: while(x>0 &&

Expression

$y \geq 1$   
 $y \leq (H[y] - 1)$   
-----

$x \geq 1$   
 $x \leq (H[x] - 1)$

Source Code

example.c

```
1: void main()  
2: {  
3:     int x,y;  
4:  
5:     x = nondet();  
6:     y = nondet();  
7:  
8:     while(x>0 && y>0) {  
9:         if (nondet()) {  
10:             y++;  
11:             x--;  
12:         } else {  
13:             y--;  
14:         }  
15:     }  
16:  
17: }
```

File: c:\sl\talks\byron\cmu6\_demo\example.c, Line: 8, Function 'main'

# Terminator Lemma Viewer

File View Help

Proof Information

[-] Lemmas

[-] main

8: while(x>0 &&

Cutpoint

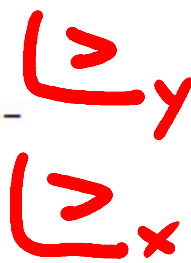
Expression

$y \geq 1$

$y \leq (H[y] - 1)$

-----  
 $x \geq 1$

$x \leq (H[x] - 1)$



Source Code

example.c

```
1: void main()
2: {
3:     int x,y;
4:
5:     x = nondet();
6:     y = nondet();
7:
8:     while(x>0 && y>0) {
9:         if (nondet()) {
10:             y++;
11:             x--;
12:         } else {
13:             y--;
14:         }
15:     }
16:
17: }
```

File: c:\sl\talks\byron\cmu6\_demo\example.c, Line: 8, Function 'main'

The screenshot shows the Terminator Lemma Viewer interface. The window title is "Terminator Lemma Viewer". It has a menu bar with "File", "View", and "Help".

The interface is divided into two main sections:

- Proof Information:**
  - A tree view under "Lemmas" shows a node "Ack" with two children: "6: n = Ack(x, y);" and "11: return Ack(x, n);".
  - An "Expression" field at the bottom left contains the text:
 

```
y >= 0
y <= (H[y] - 1)
```
- Source Code:**
  - The code is displayed in a window titled "test.c".
  - Line 6, `n = Ack(x, y);`, is highlighted in blue.
  - The code defines the Ackermann function and a main function that calls it with nondeterministic values.

At the bottom of the window, the status bar shows: "File: c:\slam\src\terminator\demos\d2\test.c, Line: 6, Function 'Ack'"

The screenshot shows the Terminator Lemma Viewer interface. The window title is "Terminator Lemma Viewer". It has a menu bar with "File", "View", and "Help".

The interface is divided into several sections:

- Proof Information:**
  - Lemmas:** A tree view showing "Ack" with two lines of code: "6: n = Ack(x,y);" and "11: return Ack(x,n);". The second line is highlighted in blue.
  - Expression:** Contains the mathematical expressions  $x \geq 0$  and  $x \leq (H[x] - 1)$ .
- Source Code:** A text editor showing the C code for the Ackermann function. Line 11, `return Ack(x,n);`, is highlighted in light blue. The code is as follows:

```

1: unsigned int Ack(unsigned int x, unsigned int y){
2:     if (x>0) {
3:         int n;
4:         if (y>0) {
5:             y--;
6:             n = Ack(x,y);
7:         } else {
8:             n = 1;
9:         }
10:        x--;
11:        return Ack(x,n);
12:    } else {
13:        return y+1;
14:    }
15: }
16:
17: void main()
18: {
19:     int x = nondet();
20:     int y = nondet();
21:     Ack(x,y);

```

At the bottom of the window, the status bar displays: "File: c:\slam\src\terminator\demos\d2\test.c, Line: 11, Function 'Ack'"

We luck out in this case, accurate support for recursion not needed

```
int y){
    x--;
    return Ack(x, y);
} else {
    return y+1;
}
}
}
void main()
{
    int x = nondet();
    int y = nondet();
    Ack(x, y);
}
```

Expression  
x >= 0  
x <= (H[x] - 1)

File: c:\slam\src\terminator\demos\d2\test.c, Line: 11, Function 'Ack'

Static Driver Verifier Defect Viewer.

File View Trace Tree Help

Trace Tree

```

main
├── 19: int x = nondet();
├── 20: int y = nondet();
└── 21: Ack
    ├── 2: if (x>0) {
    ├── 4: if (y>0) {
    ├── 8: n = 1;
    └── 11: Ack
        ├── 2: if (x>0) {
        ├── 4: if (y>0) {
        ├── 5: y--;
        └── 6: Ack
            ├── 2: if (x>0) {
            ├── 4: if (y>0) {
            └── 8: n = 1;
    
```

Step: 76

State

Lasso: Loop

Source Code

test.c

```

1: unsigned int Ack(unsigned int x, unsigned int y){
2:     if (x>0) {
3:         int n;
4:         if (y>0) {
5:             y--;
6:             n = Ack(x,y);
7:         } else {
8:             n = 1;
9:         }
10:        //x--;
11:        return Ack(x,n);
12:    } else {
13:        return y+1;
14:    }
15: }
16:
17: void main()
18: {
19:     int x = nondet();
20:     int y = nondet();
21:     Ack(x,y);
22: }
    
```

File: c:\slam\src\terminator\demos\d3\test.c, Line: 8, Function 'Ack'

Driver: Rule: Defect: Possibly non-terminating path found



Static Driver Verifier Defect Viewer.

File View Trace Tree Help

Trace Tree

```
main
├── 19: int x = nondet();
├── 20: int y = nondet();
├── 21: Ack
│   ├── 2: if (x>0) {
│   ├── 4: if (y>0) {
│   └── 8: n = 1;
├── 11: Ack
│   ├── 2: if (x>0) {
│   ├── 4: if (y>0) {
│   ├── 5: y--;
│   └── 6: Ack
│       ├── 2: if (x>0) {
│       ├── 4: if (y>0) {
│       └── 8: n = 1;
```

stem

cycle

Source Code

test.c

```
1: unsigned int Ack(unsigned int x, unsigned int y){
2:     if (x>0) {
3:         int n;
4:         if (y>0) {
5:             y--;
6:             n = Ack(x,y);
7:         } else {
8:             n = 1;
9:         }
10:        //x--;
11:        return Ack(x,n);
12:    } else {
13:        return y+1;
14:    }
15: }
16:
17: void main()
18: {
19:     int x = nondet();
20:     int y = nondet();
21:     Ack(x,y);
22: }
```

Step: 76

State

Lasso: Loop

File: c:\slam\src\terminator\demos\d3\test.c, Line: 8, Function 'Ack'

Driver: Rule: Defect: Possibly non-terminating path found

# Examples

The screenshot shows the Terminator Lemma Viewer interface. The window title is "Terminator Lemma Viewer". The menu bar includes "File", "View", and "Help".

**Proof Information**

- [-] Lemmas
  - [-] main
    - 8: while (x>0)

**Expression**

```
x>=1
x<=(H[x]-1)
-----
y>=(-1)
y<=(H[y]-1)
```

**Source Code**

phase.c

```
1: void main()
2: {
3:     int x,y;
4:
5:     x = nondet();
6:     y = nondet();
7:
8:     while (x>0) {
9:
10:        if (y<0) {} else {}
11:
12:        x = x + y;
13:        y--;
14:    }
15: }
```

File: c:\sl\talks\byron\cmu6\_demo2\phase.c, Line: 8, Function 'main'



# Examples

The screenshot shows the Terminator Lemma Viewer interface. On the left, the 'Proof Information' pane shows a tree of lemmas with '8: while (x>0)' selected. Below it, the 'Expression' pane shows the invariant  $x \geq 1$  and  $x \leq (H[x] - 1)$ , and  $y \geq (-1)$  and  $y \leq (H[y] - 1)$ . The main 'Source Code' pane displays the C code for 'phase.c', with line 8 highlighted. Handwritten red text 'Notice the trick' with an arrow points to the 'if (y<0) {} else {}' statement on line 10, which is also circled in red. The status bar at the bottom indicates the file path and current location: 'File: c:\sl\talks\byron\cmu6\_demo2\phase.c, Line: 8, Function 'main''.

```
1: void main()
2: {
3:     int x,y;
4:
5:     x = nondet();
6:     y = nondet();
7:
8:     while (x>0) {
9:
10:        if (y<0) {} else {}
11:
12:        x = x + y;
13:        y--;
14:    }
15: }
```

Notice the trick

File: c:\sl\talks\byron\cmu6\_demo2\phase.c, Line: 8, Function 'main'

# Overview

- Notes on a representation for programs
- Checking termination arguments
- Refining termination arguments
- Induction
- Termination analysis

# Induction

```
 $T := \emptyset$   
while REACHABLE( $\boxplus(R, \ell, T), \ell_{err}$ ) do  
  let  $\pi_s, \pi_c =$  lasso in  $\boxplus(R, \ell, T)$  from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^*(\llbracket \pi_s \rrbracket(\top)))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \cap \rho \times \rho$ ) returns ranking function  $f$  then  
     $T := T \cup \sqsupseteq_f$   
  else  
    report “potential counterexample found:  $\pi_s, \pi_c$ ”  
  fi  
od  
report “termination proved with argument  $T$ ”
```

# Induction

```
 $T := \emptyset$   
while REACHABLE( $\boxplus(R, \ell, T), \ell_{err}$ ) do  
  let  $\pi_s, \pi_c =$  lasso in  $\boxplus(R, \ell, T)$  from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^*(\llbracket \pi_s \rrbracket(\top)))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \cap \rho \times \rho$ ) returns ranking function  $f$  then  
     $T := T \cup \geq_f$   
  else  
    report “potential counterexample found:  $\pi_s, \pi_c$ ”  
  fi  
od  
report “termination proved with argument  $T$ ”
```

# Induction

Induction check

```
 $T := \emptyset$   
while REACHABLE( $\boxplus(R, \ell, T)$ ,  $\ell_{err}$ ) do  
  let  $\pi_s, \pi_c =$  lasso in  $\boxplus(R, \ell, T)$  from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket (\top)))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \cap \rho \times \rho$ ) returns ranking function  $f$  then  
     $T := T \cup \geq_f$   
  else  
    report “potential counterexample found:  $\pi_s, \pi_c$ ”  
  fi  
od  
report “termination proved with argument  $T$ ”
```

# Induction

Induction check

```
 $T := \emptyset$   
while REACHABLE( $\boxplus(R, \ell, T), \ell_{err}$ ) do  
  let  $\pi_s, \pi_c = \text{lasso in } \boxplus(R, \ell, T)$  from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket (\top)))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \cap \rho \times \rho$ ) returns ranking function  $f$  then  
     $T := T \cup \geq_f$   
  else  
    report “potential counterexample found:  $\pi_s, \pi_c$ ”  
  fi  
od  
report “termination proved with argument  $T$ ”
```

# Induction

Induction check

```
 $T := \emptyset$   
while REACHABLE( $\boxplus(R, \ell, T), \ell_{err}$ ) do  
  let  $\pi_s, \pi_c = \text{lasso in } \boxplus(R, \ell, T)$  from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket(T)))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \cap \rho \times \rho$ ) returns ranking function  $f$  then  
     $T := T \cup \geq_f$   
  else  
    report “potential counterexample found:  $\pi_s, \pi_c$ ”  
  fi  
od  
report “termination proved with argument  $T$ ”
```

# Induction

Induction check

```
 $T := \emptyset$   
while REACHABLE( $\boxplus(R, \ell, T), \ell_{err}$ ) do  
  let  $\pi_s, \pi_c = \text{lasso in } \boxplus(R, \ell, T)$  from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket(T)))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \cap \rho \times \rho$ ) returns ranking function  $f$  then  
     $T := T \cup \geq_f$   
  else  
    report “potential counterexample found:  $\pi_s, \pi_c$ ”  
  fi  
od  
report “termination proved with argument  $T$ ”
```



# Induction

Induction check

```
 $T := \emptyset$   
while REACHABLE( $\boxplus(R, \ell, T), \ell_{err}$ ) do  
  let  $\pi_s, \pi_c =$  lasso in  $\boxplus(R, \ell, T)$  from 0 to  $\ell$ , and  $\ell$  to  $\ell_{err}$   
  let  $\rho = \alpha(\llbracket \pi_c \rrbracket^* (\llbracket \pi_s \rrbracket(T)))$   
  if SYNTHESIS( $\llbracket \pi_c \rrbracket \cap \rho \times \rho$ ) returns ranking function  $f$  then  
     $T := T \cup \{ \rho \}$   $\wedge \rho \succeq_f \rho$  for all functions  $f$  which are preserved  
  else  
    report "potential counterexample found:  $\pi_s, \pi_c$ "  
  fi  
od  
report "termination proved with argument  $T$ "
```

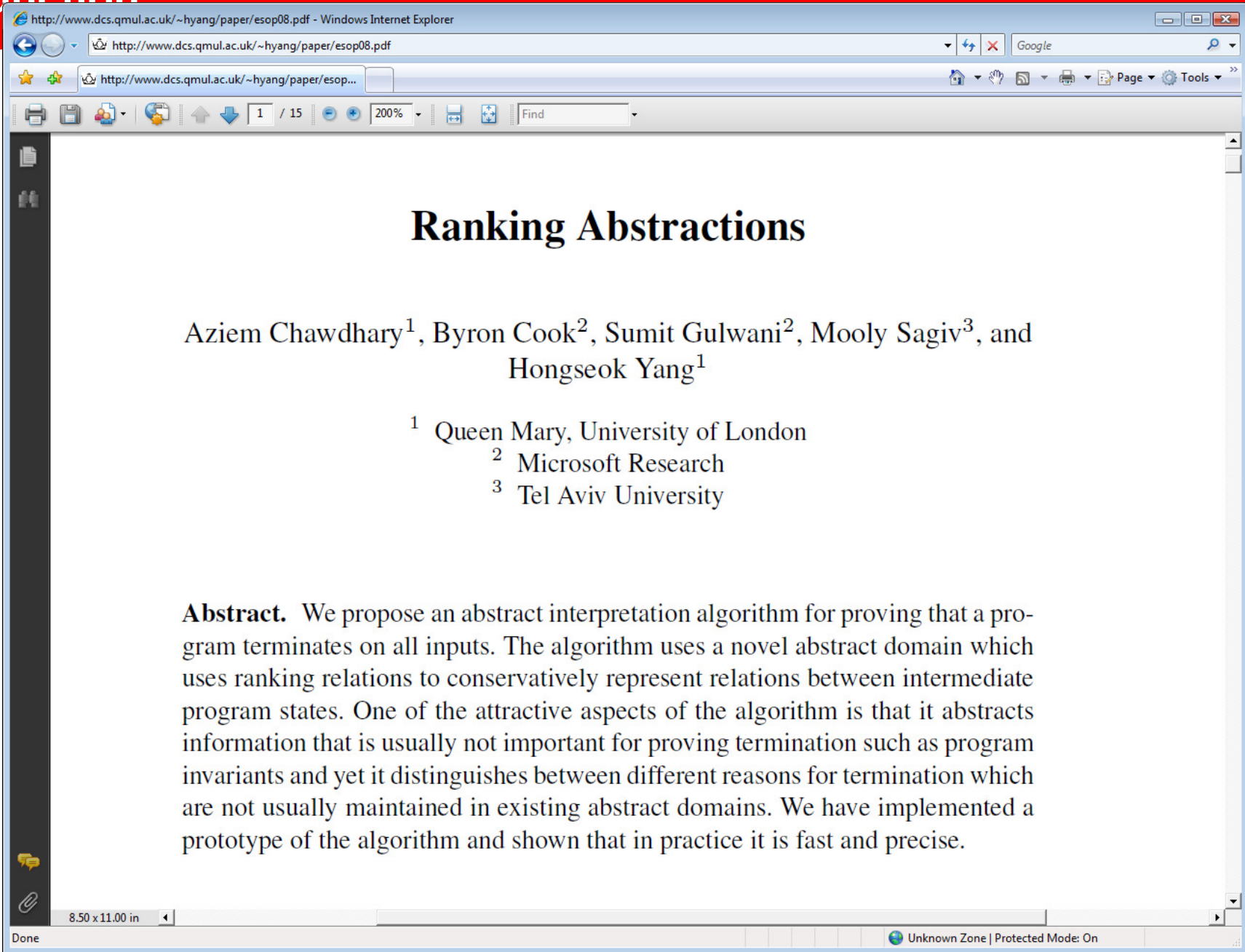
# Induction

Induction check

```
T := ∅
while REACHABLE(⊕(R, l, T), l_err) do
  let π_s, π_c = lasso in ⊕(R, l, T) from 0 to l, and l to l_err
  let ρ = α(⟦π_c⟧*(⟦π_s⟧(T)))
  if SYNTHESIS(⟦π_c⟧(ρ × ρ)) returns ranking function f then
    T := T ∪ ≥_f
  else
    report "potential counterexample found: π_s, π_c"
  fi
od
report "termination proved with argument T"
```

for all functions  $f$  which are preserved

Could use the invariant at location  $l$



The screenshot shows a Windows Internet Explorer browser window. The address bar contains the URL <http://www.dcs.qmul.ac.uk/~hyang/paper/esop08.pdf>. The browser's toolbar includes navigation buttons (back, forward, home, stop), a search box with the text "Google", and various utility icons. The main content area displays the title "Ranking Abstractions" in a large, bold, black serif font. Below the title, the authors are listed: "Aziem Chawdhary<sup>1</sup>, Byron Cook<sup>2</sup>, Sumit Gulwani<sup>2</sup>, Mooly Sagiv<sup>3</sup>, and Hongseok Yang<sup>1</sup>". The affiliations are listed below: "<sup>1</sup> Queen Mary, University of London", "<sup>2</sup> Microsoft Research", and "<sup>3</sup> Tel Aviv University". The abstract text follows: "**Abstract.** We propose an abstract interpretation algorithm for proving that a program terminates on all inputs. The algorithm uses a novel abstract domain which uses ranking relations to conservatively represent relations between intermediate program states. One of the attractive aspects of the algorithm is that it abstracts information that is usually not important for proving termination such as program invariants and yet it distinguishes between different reasons for termination which are not usually maintained in existing abstract domains. We have implemented a prototype of the algorithm and shown that in practice it is fast and precise." The browser's status bar at the bottom shows "Done" on the left and "Unknown Zone | Protected Mode: On" on the right. The page number "79" is visible in the bottom-left corner of the overall image.

## The bad news

Bad news: lassos don't always  
lead to progress .....

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$$R_I^+ \subseteq \supseteq_{f_1} \cup \supseteq_{f_2} \cup \dots \cup \supseteq_{f_n}$$

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$$R_I^+ \subseteq \supseteq_{f_1} \cup \supseteq_{f_2} \cup \dots \cup \supseteq_{f_n} \\ \cup \supseteq_{f_{n+1}} \cup \dots ?$$

## The bad news

Bad news: lassos don't always  
lead to progress .....

```
1  while(x>0) {
2      y = x;
3      while(y>0) {
4          y = y - 1;
5      }
6      x = x + 1;
7 }
```

## The bad news

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7   }
```

1  
2  
3  
4  
3  
4  
3  
6

## The bad news

Bad news: lassos don't always  
lead to progress .....

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1  while(x>0) {  
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```

```
1 :  $x_0 > 0$   
2 :  $y_1 = x_0$   
3 :  $y_1 > 0$   
4 :  $y_2 = y_1 - 1$   
3 :  $y_2 > 0$   
4 :  $y_3 = y_2 - 1$   
3 :  $y_3 \leq 0$   
6 :  $x_1 = x_0 + 1$ 
```

## The bad news

Bad lead always

$y_2 = 1$   
 $y_3 = 0$

```
1  while(x>0) {  
2      y = x;  
3      while(y>0) {  
4          y = y - 1;  
5      }  
6      x = x + 1;  
7  }
```

```
1:  $x_0 > 0$   
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3:  $y_2 > 0$   
4:  $y_3 = y_2 - 1$   
3:  $y_3 \leq 0$   
6:  $x_1 = x_0 + 1$ 
```

## The bad news

Bad

lead

$$y_1 = 2$$

$$y_2 = 1$$

$$y_3 = 0$$

always

```
1 while(x>0) {
2     y = x;
3     while(y>0) {
4         y = y - 1;
5     }
6     x = x + 1;
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```
1:  $x_0 > 0$ 
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3:  $y_2 > 0$ 
4:  $y_3 = y_2 - 1$ 
3:  $y_3 \leq 0$ 
6:  $x_1 = x_0 + 1$ 
```

## The bad news

Bad

lead

$$y_1 = 2 \quad x_0 = 2$$

$$y_2 = 1$$

$$y_3 = 0$$

always

```
1  while(x>0) {
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3:  $y_3 \leq 0$ 
6:  $x_1 = x_0 + 1$ 
```

## The bad news

Bad

lead

$$\begin{array}{l} y_1 = 2 \\ y_2 = 1 \\ y_3 = 0 \end{array} \quad \begin{array}{l} x_0 = 2 \\ x_1 = 3 \end{array}$$

always

```
1  while(x>0) {
2      y = x;
3      while(y>0) {
4          y = y - 1;
5      }
6      x = x + 1;
7  }
```

```
1:  $x_0 > 0$ 
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3:  $y_1 > 0$ 
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3:  $y_2 > 0$ 
4:  $y_3 = y_2 - 1$ 
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```

## The bad news

Bad

lead

$$\begin{array}{l} y_1 = 2 \\ y_2 = 1 \\ y_3 = 0 \end{array} \quad \begin{array}{l} x_0 = 2 \\ x_1 = 3 \end{array}$$

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3:   $y_2 > 0$ 
4:   $y_3 = y_2 - 1$ 
3:   $y_3 \leq 0$ 
6:   $x_1 = x_0 + 1$ 
```

Thus: trivially well founded

## More bad news

Standard symbolic model checkers  
don't really like the validity checking  
task



## More bad news

Standard symbolic model checkers  
don't really like the validity checking  
task

$$R_I^+ \subseteq \bigvee_{f_1} \cup \bigvee_{f_2} \cup \dots \cup \bigvee_{f_n}$$

## More bad news

Standard

don't

task

SLAM usually doesn't terminate unless modified significantly

$$R_I^+ \subseteq \bigcup_{f_1} \cup \bigcup_{f_2} \cup \dots \cup \bigcup_{f_n}$$

# Overview

- Notes on a representation for programs
- Checking termination arguments
- Refining termination arguments
- Induction
- Termination analysis

## Transformations to reachability supporting closure

```
copied = 0;  
.  
.  
.  
while(*) {  
    if (copied==0) {  
        if (*) {  
            old_x = x;  
            old_y = y;  
            .  
            .  
            copied = 1;  
        }  
    } else {  
        assert(Q);  
    }  
    • loop body  
}
```

# Transformations to reachability supporting closure

```
copied = 0;
.
.
.

while(*) {
    if (copied==0) {
        if (*) {
            old_x = x;
            old_y = y;
            .
            .
            copied = 1;
        }
    } else {
return skip;
    }
}
• loop body
```

# Transformations to reachability supporting closure

```
copied = 0;  
.  
.  
.  
  
while(*) {  
    if (copied==0) {  
        if (*) {  
            old_x = x;  
            old_y = y;  
            .  
            .  
            copied = 1;  
        }  
    } else {  
        skip;  
    }  
    .  
    loop body  
}
```

If the invariant  
here is disjunctively  
WF, then we  
have proved  
termination

# Transformations to reachability supporting closure

```
copied = 0;  
.  
.  
.  
  
while(*) {  
    if (copied==0) {  
        if (*) {  
            old_x = x;  
            old_y = y;  
            .  
            .  
            copied = 1;  
        }  
    } else {  
        return skip;  
    }  
    .  
    .  
    .  
    • loop body  
}
```

Standard analysis techniques can over approximate these states

# Transformations to reachability supporting closure

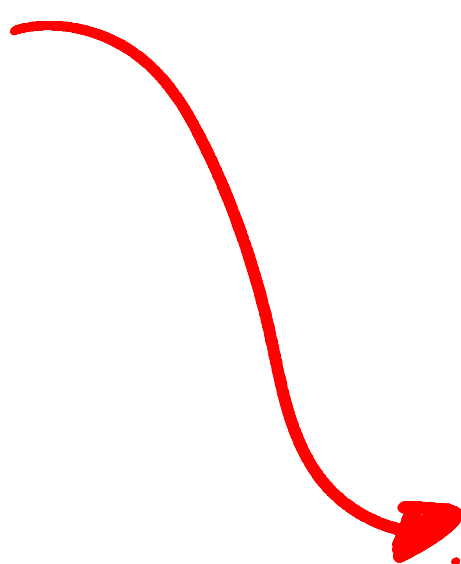
*assume (inv)*

*old\_x = x*

*old\_y = y*

*goto*

```
copied = 0;  
.  
.  
.  
while(*) {  
  if (copied==0) {  
    if (*) {  
      old_x = x;  
      old_y = y;  
      .  
      .  
      copied = 1;  
    }  
  } else {  
    goto skip;  
  }  
  .  
  loop body  
}
```





# Transformations to reachability supporting closure

*assume(inv)*  
*old\_x = x*  
*old\_y = y*  
*goto*

`copied = 0;`

`.`  
`.`

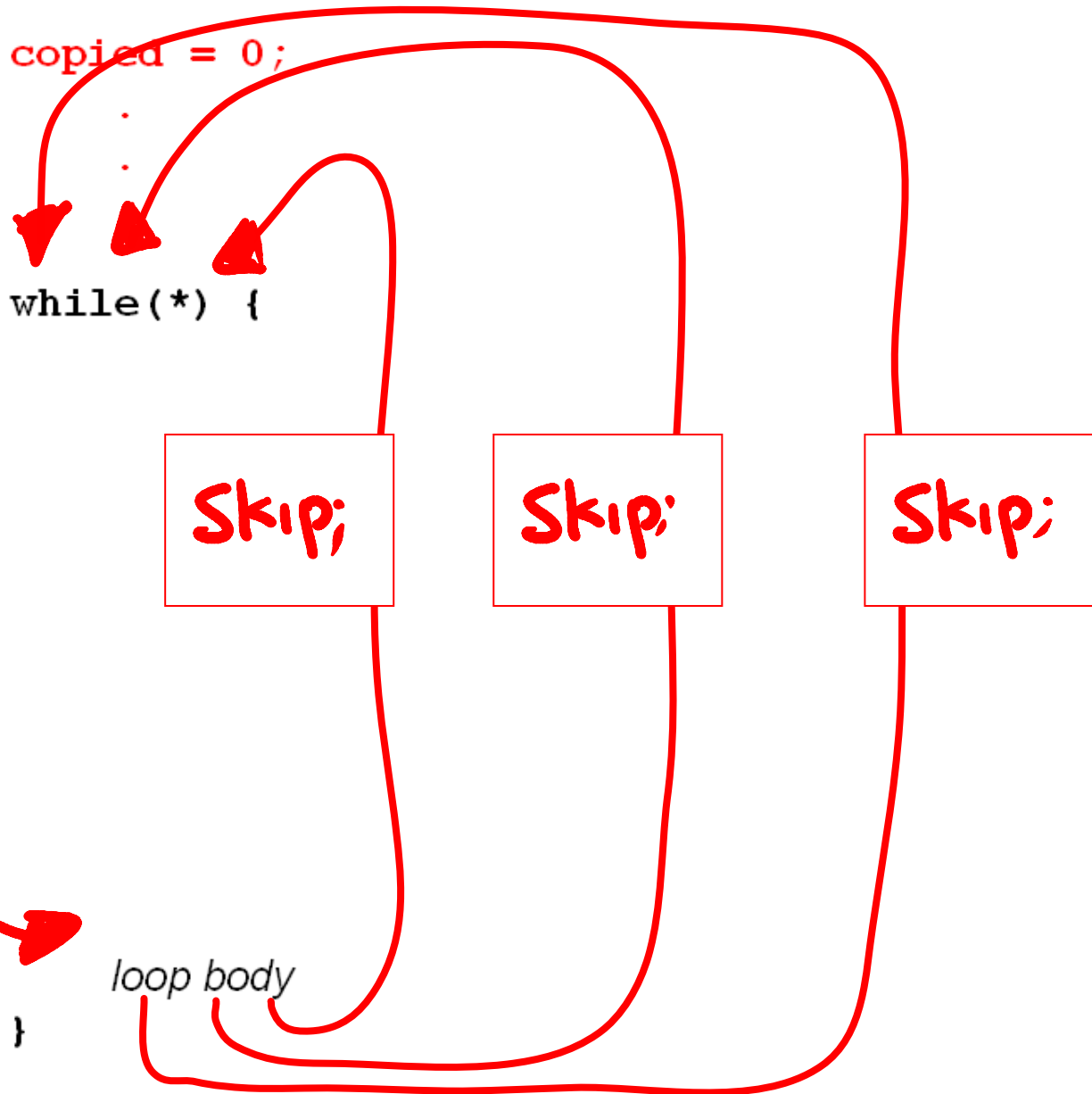
`while(*) {`

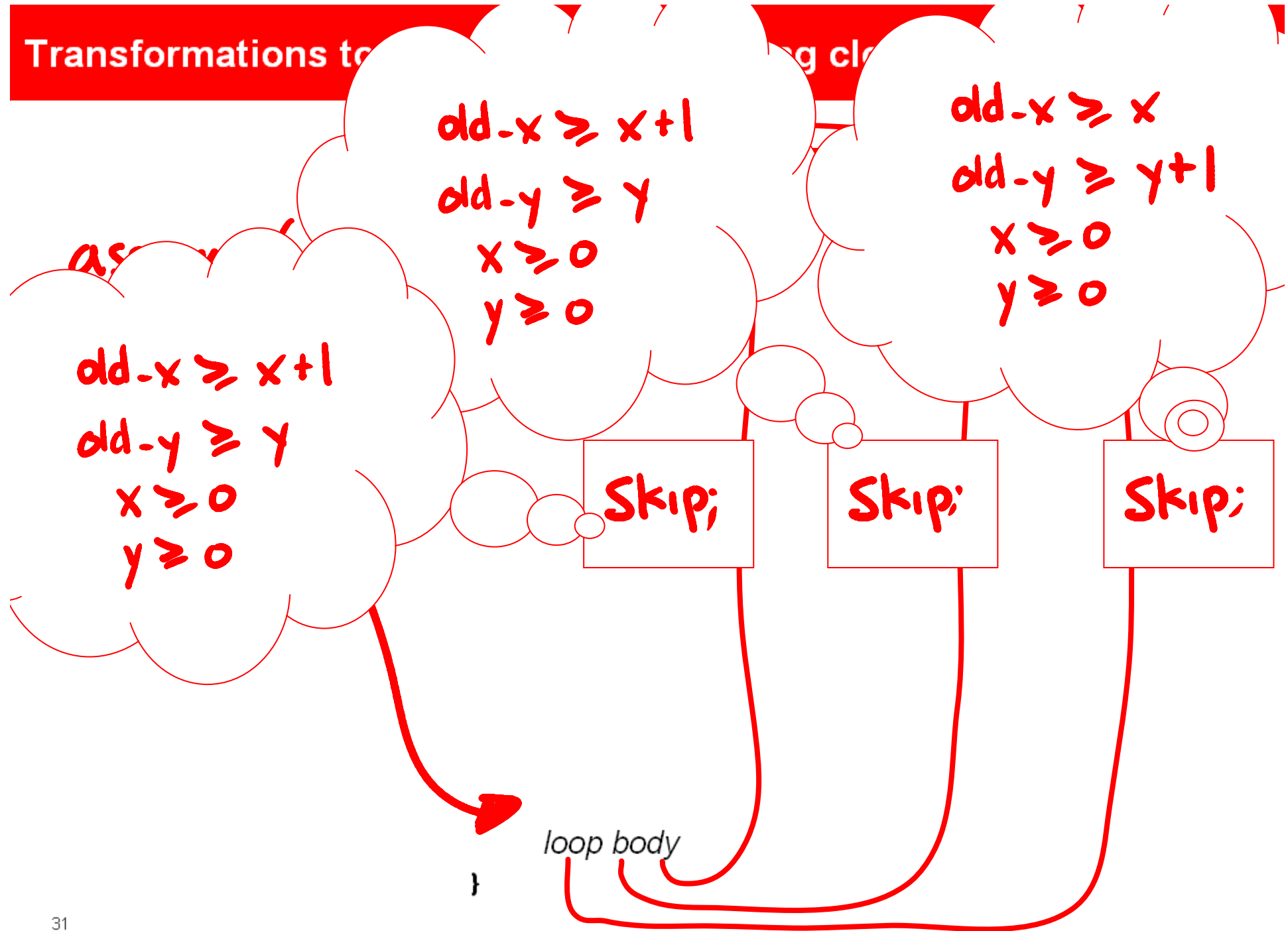
*loop body*

`}`

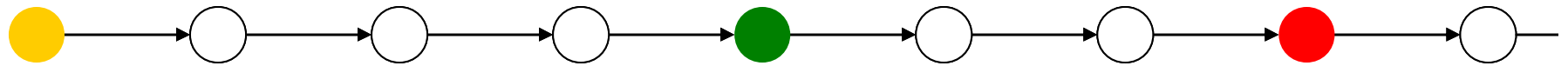
# Transformations to reachability supporting closure

*assume(inv)*  
*old\_x = x*  
*old\_y = y*  
*goto*

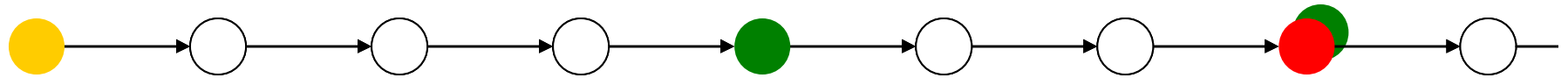




# Variance analysis

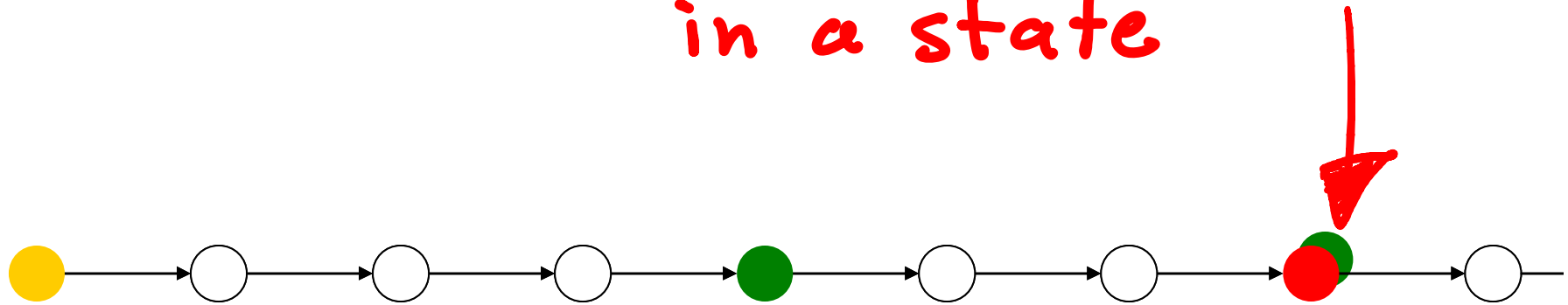


# Variance analysis

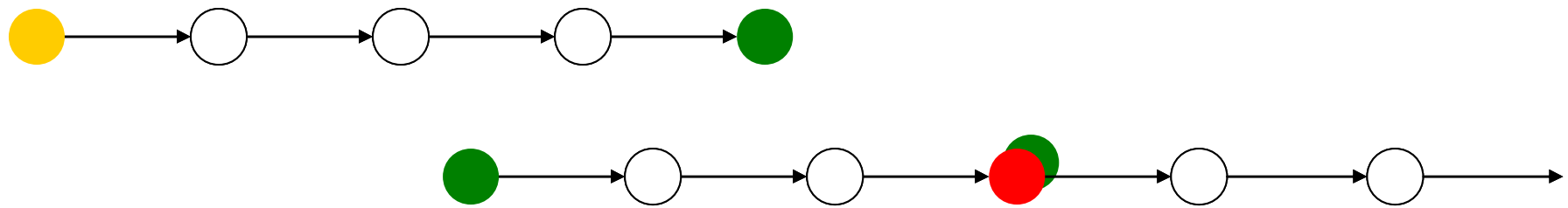


## Variance analysis

Overapproximation  
of relation encoded  
in a state

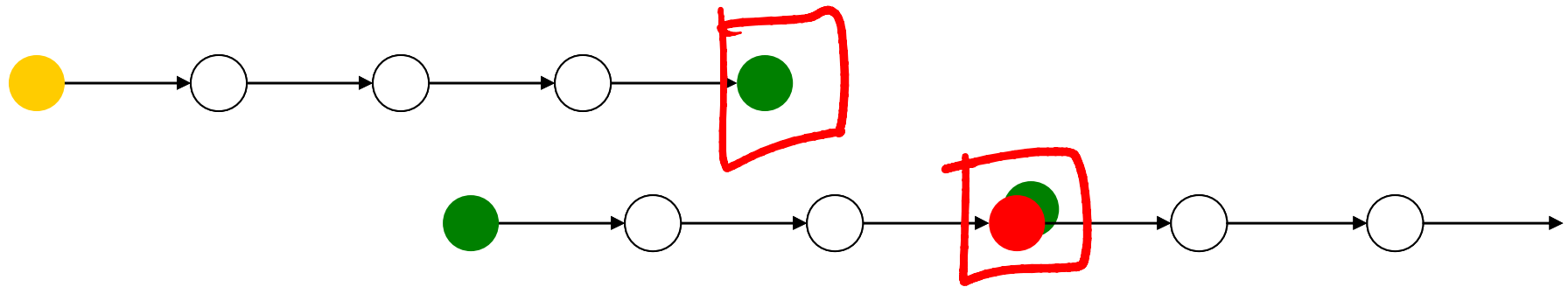


# Variance analysis



## Variance analysis

Use off-the-shelf abstract interpretation techniques to compute inclusion





# Variance analysis

$$R_{R(I)}^+$$

$$\mathbb{R}^+ \subseteq \mathbb{R}_{f_1} \cup \mathbb{R}_{f_2} \cup \dots \cup \mathbb{R}_{f_n}$$

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1 / 14 150% Find

# Variance Analyses From Invariance Analyses

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## Abstract

An invariance assertion for a program location  $\ell$  is a statement that always holds at  $\ell$  during execution of the program. Program invariance analyses infer invariance assertions that can be useful when trying to prove safety properties. We use the term *variance assertion* to mean a statement that holds between any state at  $\ell$  and any previous state that was also at  $\ell$ . This paper is concerned with the development of analyses for variance assertions and their application to proving termination and liveness properties. We describe

the user (or algorithm calling the invariance analysis) might try to refine the abstraction. For example, if the tool is based on abstract interpretation they may choose to improve the abstraction by delaying the widening operation [28], using dynamic partitioning [33], employing a different abstract domain, etc.

The aim of this paper is to develop an analogous set of tools for program termination and liveness: we introduce a class of tools called *variance analyses* which infer assertions, called *variance assertions*, that hold between any state at a location  $\ell$  and any

8.27 x 11.69 in  
Done  
Unknown Zone | Protected Mode: On

```
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http://research.microsoft.com/en-us/um/cambridge/projects/terminator/pop107a.pdf
http://research.microsoft.com/en-us/um/cambri...
2 / 14 300% Find
01 VARIANCEANALYSIS( $P, L, I^\#$ ) {
02    $IAs :=$  INVARIANCEANALYSIS( $P, I^\#$ )
03   foreach  $\ell \in L$  {
04      $LTPreds[\ell] :=$  true
05      $O :=$  ISOLATE( $P, L, \ell$ )
06     foreach  $q \in IAs$  such that  $pc(q) = \ell$  {
07        $VAs :=$  INVARIANCEANALYSIS( $O, STEP(O, \{SEED(q)\})$ )
08       foreach  $r \in VAs$  {
09         if  $pc(r) = \ell \wedge \neg WELLFOUNDED(r)$  {
10            $LTPreds[\ell] :=$  false
11         }
12       }
13     }
14   }
15   return  $LTPreds$ 
16 }
```

8.27 x 11.69 in Done Unknown Zone | Protected Mode: On

# Variance analysis

## → Strategy:

- Use abstract interpretation techniques to compute (disjunctive) overapproximation
- Check that the parts of the disjunction are well founded

## → Advantages:

- Can use existing abstract interpretation tools to compute overapproximation
- Always terminates
- Fast

## → Disadvantages:

- No counterexamples
- Less accurate than refinement-based approach
- Abstract domains (currently) not built for our application
  - Widening can be too aggressive
  - Redundant information kept

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http://research.microsoft.com/en-us/um/people/gurevich/Opera/178.pdf

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2 / 25 147% Find

the smallest ordinal  $\alpha$  such that  $\pi$  admits a ranking function with values  $< \alpha$  is the *ranking height* of  $\pi$ . The following observation may be helpful in establishing termination.

LEMMA 1 (COVERING OBSERVATION). *Any transitive relation covered by finitely many well-founded relations is well-founded.*

In other words, if relations  $U_1, \dots, U_n$  are well-founded and  $R \subseteq U_1 \cup \dots \cup U_n$  is a transitive relation, then  $R$  is well-founded.

Apparently this observation was made independently a number of times, and each time it was related to the termination problem. As far as we know, the observation was made first by Alfons Geser in [1990, page 31]. A weaker form of the observation, in which the relations  $U_i$  are required to be transitive, had been proposed as a question on the web by Geser, and he informed us that he received proofs of it from Jean-Pierre Jouannaud, Werner Nutt, Franz Baader, George McNulty, Thomas Streicher, and Dieter Hofbauer; see [Lescanne, discussion list, items 38–42] for all but the last two of these. Both of our two referees pointed out that the observation was made independently in [Lee et al. 2001]. One of them wrote that “the covering observation lies at the heart of” [Lee et al. 2001] where it “is used implicitly in Theorem 4.” The other referee pointed out that the covering observation was made independently in [Dershowitz et al. 2001] and in [Codish et al. 2003]; see [Bruynooghe et al. ] in this connection. Recently the covering observation was rediscovered in [Podelski and Rybalchenko 2004] and was used for proving termination in [Podelski and Rybalchenko 2005; Cook et al. 2006; Berdine et al. 2007]. A stronger version of the covering observation, using a hypothesis that is weaker (but more complicated) than transitivity of  $R$ , was given in [Doornbos and Von Karger 1998].

The covering observation is proved by a straightforward application of the infinite version of Ramsey’s theorem. The transitivity of  $R$  is essential here. If  $a, b$  are distinct elements then the relation  $\{(a, b), (b, a)\}$  is covered by the well-founded relations  $\{(a, b)\}$  and  $\{(b, a)\}$  but is not well-founded.

*Example 2.* Let  $\pi_1$  be the program



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2 / 25 147%

Find

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the smallest ordinal  $\alpha$  such that  $\pi$  admits a ranking function with values  $< \alpha$  is the *ranking height* of  $\pi$ . The following example may be helpful in establishing termination.

What about size-change?

George Gurevich, Thomas Lescanne, discussion list, items 38–42] for all but the last two of two referees pointed out that the observation was made independently in [Lee et al. 2001]. One of them wrote that “the covering observation lies at the heart of” [Lee et al. 2001] where it “is used implicitly in Theorem 4.” The other referee pointed out that the covering observation was made independently in [Dershowitz et al. 2001] and in [Codish et al. 2003]; see [Bruynooghe et al. ] in this connection. Recently the covering observation was rediscovered in [Podelski and Rybalchenko 2004] and was used for proving termination in [Podelski and Rybalchenko 2005; Cook et al. 2006; Berdine et al. 2007]. A stronger version of the covering observation, using a hypothesis that is weaker (but more complicated) than transitivity of  $R$ , was given in [Doornbos and Von Karger 1998].

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*Example 2.* Let  $\pi_1$  be the program

## Termination proof rule

$$R^+ \subseteq \sqsupseteq_a \cup \sqsupseteq_b \cup \dots \cup \sqsupseteq_z$$



# Termination proof rule

$$R^+ \subseteq \underbrace{\sqsupseteq}_a \cup \underbrace{\sqsupseteq}_b \cup \dots \cup \underbrace{\sqsupseteq}_z$$

All program variables used

# Termination proof rule

$$\cancel{R^+} \subseteq \underbrace{\triangleright_a}_{d(R)^+} \cup \underbrace{\triangleright_b}_{\text{All program variables used}} \cup \dots \cup \underbrace{\triangleright_z}_{\text{All program variables used}}$$

The diagram shows a mathematical expression for a termination proof rule. The left side is  $\cancel{R^+}$ , where the plus sign and the entire term are crossed out with red diagonal lines. This is followed by a subset symbol  $\subseteq$  and a union of well-founded relations:  $\triangleright_a \cup \triangleright_b \cup \dots \cup \triangleright_z$ . Each relation  $\triangleright_x$  is underlined in red. Below the underlines, red arrows point from the text  $d(R)^+$  to  $\triangleright_a$ , and from the text "All program variables used" to both  $\triangleright_b$  and  $\triangleright_z$ .